

LIST OF EXPERIMENTS

1. Verification of Boolean theorems using digital logic gates
2. Design and implementation of combinational circuits using basic gates for arbitrary functions, code converters, etc.
3. Design and implementation of 4-bit binary adder / subtractor using basic gates and MSI devices
4. Design and implementation of parity generator / checker using basic gates and MSI devices
5. Design and implementation of magnitude comparator
6. Design and implementation of application using multiplexers/ Demultiplexers
7. Design and implementation of Shift registers
8. Design and implementation of Synchronous and Asynchronous counters
9. Simulation of combinational circuits using Hardware Description Language (VHDL/ Verilog HDL software required)
10. Simulation of sequential circuits using HDL (VHDL/ Verilog HDL software required)

(Common to Information Technology & Computer Science Engineering)

List of equipments and components for a batch of 30 students (2 per batch)

S.NO	Name of equipment/ component	Quantity Reqd	Remarks
1	Dual power supply/ single mode powersupply	15/30	+12/-12V
2	IC Trainer	15	10 bit
3	Bread Boards	15	
4	Multimeter	5	
6	IC 7400	60	
7	IC7402	60	
8	IC 7404	60	
9	IC 7486	60	
10	IC 7408	60	
11	IC 7432	60	
12	IC 7483	60	
13	IC74150	60	

14	IC74151	40	
15	IC74147	40	
16	IC7445	40	
17	IC7476	40	
18	IC7491	40	
19	IC555	40	
20	IC7494	40	
21	IC7447	40	
22	IC74180	40	
23	IC7485	40	
24	IC7473	40	
25	IC74138	40	
26	IC7411	40	
27	IC7474	40	
28	Computer with HDL software	30	
29	Seven segment display	40	
30	Assembled LED board/LEDs	40/200	
31	Wires		Single strand